

	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Inventor
1	US 20040120424 A1	20040624	68	Method for making a clear channel assessment in a wireless network	375/327	375/345	Roberts, Richard D.
2	US 20030128777 A1	20030710	25	Decision directed phase locked loops (DD-PLL) with multiple initial phase and/or frequency estimates in digital communication systems	375/327	375/376	Linsky, Stuart T. et al.
3	US 20030112899 A1	20030619	26	Decision directed phase locked loops (DD-PLL) with excess processing power in digital communication systems	375/327	327/156; 329/307; 375/376	Linsky, Stuart T. et al.
4	US 20030103582 A1	20030605	24	Selective reed-solomon error correction decoders in digital communication systems	375/327		Linsky, Stuart T. et al.
5	US 20030016767 A1	20030123	12	Phase tracker for linearly modulated signal	375/326	375/327	Houtman, Tom
6	US 6236687 B1	20010522	20	Decision directed phase locked loop (DD-PLL) for use with short block codes in digital communication systems	375/327	375/332; 375/333; 375/340; 375/376	Caso; Gregory S. et al.
7	US 5157694 A	19921020	9	Coherent PSK demodulator using bit error rate and S/N ratio to establish synchronization	375/327	329/307; 375/329; 375/331; 714/789	Iwasaki; Motoya et al.
8	US 5025455 A	19910618	18	Phase ambiguity resolution for offset QPSK modulation systems	375/327	329/304; 375/332	Nguyen; Tien M.

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1	US 20040240588 A1	20041202	65	Compensation for non-linear distortion in a modem receiver	375/340	375/350	Miller, William J.
2	US 20030177409 A1	20030918	31	Self-timed pipeline for tunable delays	713/401		Greenstreet, Mark R.
3	US 20030133514 A1	20030717	50	Apparatus and method for decode arbitration in a multi-stream multimedia system	375/260		Lais, Eric et al.
4	US 20030081705 A1	20030501	39	Compensation for non-linear distortion in a modem receiver	375/346		Miller, William J.
5	US 20020130944 A1	20020919	49	Light-emission modulation having effective scheme of creating gray scale on image	347/132	347/248	Ema, Hidetoshi et al.
6	US 6754746 B1	20040622	42	Memory array with read/write methods	710/100	365/206; 365/230.01; 365/230.03; 365/233; 710/305; 710/307; 711/103; 711/151; 714/759	Leung; Wingyu et al.

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7	US 6735264 B2	20040511	38	Compensation for non-linear distortion in a modem receiver	375/340	329/319; 329/320; 375/349; 375/350; 455/303; 455/307; 455/43; 455/501	Miller; William J.
8	US 6731317 B2	20040504	48	Pulse modulation signal generation circuit, and semiconductor laser modulation device, optical scanning device and image formation device using the same	347/135	347/132	Ema; Hidetoshi et al.
9	US 6690309 B1	20040210	28	High speed transmission system with clock inclusive balanced coding	341/102	341/50	James; David Vernon et al.
10	US 6388587 B1	20020514	22	Partial response channel having combined MTR and parity constraints	341/59	714/802	Brickner; Barrett J. et al.

	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Inventor
11	US 6359525 B1	20020319	15	Modulation technique for transmitting multiple high data rate signals through a band limited channel	332/108	329/312; 329/313; 332/109; 332/112; 370/212; 370/213; 375/238; 375/239; 375/242; 375/340	Mohan; Chandra et al.
12	US 6356555 B1	20020312	110	Apparatus and method for digital data transmission using orthogonal codes	370/441	370/442; 370/478; 370/479; 370/480; 370/503	Rakib; Selim Shlomo et al.
13	US 6288668 B1	20010911	56	Analog to digital converter, encoder, and recorded data reproducing apparatus	341/172	341/159	Tsukamoto; Sanroku et al.
14	US 6281721 B1	20010828	12	Programmable frequency divider	327/115	327/117; 327/147	Kinget; Peter R. et al.

	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Inventor
15	US 6154456 A	20001128	43	Apparatus and method for digital data transmission using orthogonal codes	370/342	370/350; 370/479; 370/508; 370/528	Rakib; Selim Shlomo et al.
16	US 5991308 A	19991123	161	Lower overhead method for data transmission using ATM and SCDMA over hybrid fiber coax cable plant	370/395.53	370/342; 370/395.52; 370/395.65; 370/474	Fuhrmann; Amir Michael et al.
17	US 5966376 A	19991012	41	Apparatus and method for digital data transmission using orthogonal cyclic codes	370/342	370/441; 370/479; 375/261; 375/298; 375/358; 714/746; 714/795; 725/144	Rakib; Selim Shlomo et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
18	US 5805583 A	19980908	45	Process for communicating multiple channels of digital data in distributed systems using synchronous code division multiple access	370/342	370/335; 370/347; 370/441; 370/479; 375/261; 375/298; 375/358	Rakib; Selim Shlomo
19	US 5793759 A	19980811	65	Apparatus and method for digital data transmission over video cable using orthogonal cyclic codes	370/342	370/441; 725/111; 725/126	Rakib; Selim Shlomo et al.
20	US 5786732 A	19980728	26	Phase locked loop circuitry including a multiple frequency output voltage controlled oscillator circuit	331/1A	327/105; 327/116; 327/159; 331/177 R; 331/34; 331/57; 331/74	Nielson; Edward T.
21	US 5768269 A	19980616	43	Apparatus and method for establishing frame synchronization in distributed digital data communication systems	370/342	370/441; 370/508; 370/509	Rakib; Selim Shlomo et al.
22	US 5745837 A	19980428	76	Apparatus and method for digital data transmission over a CATV system using an ATM transport protocol and SCDMA	725/114	725/129; 725/131	Fuhrmann; Amir Michael

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
23	US 5646557 A	19970708	7	Data processing system and method for improving performance of domino-type logic using multiphase clocks	326/97	326/96; 326/98	Runyon; Stephen Larry et al.
24	US 5321750 A	19940614	84	Restricted information distribution system apparatus and methods	380/230	348/476; 380/240; 725/109; 725/25; 725/37	Nadan; Joseph S.
25	US 4105995 A	19780808	186	Digitally controlled transmission impairment measuring apparatus	714/714	379/28	Bothof; Delwin L. et al.